## Notice of References Cited Application/Control No. O9/974,559 Examiner Paul B Yanchus Applicant(s)/Patent Under Reexamination CHANG, NAI-SHUNG Art Unit Page 1 of 1

## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,269,443	07-2001	Poisner et al.	713/1
	В	US-5,862,351	01-1999	He, Zhi Qiang	710/104
	С	US-			
	D	US-			
	Е	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	R					
	S					
	T					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	"AMD Duron Processor Data Sheet", September 2000, pages 33-35		
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	w			
	х			

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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